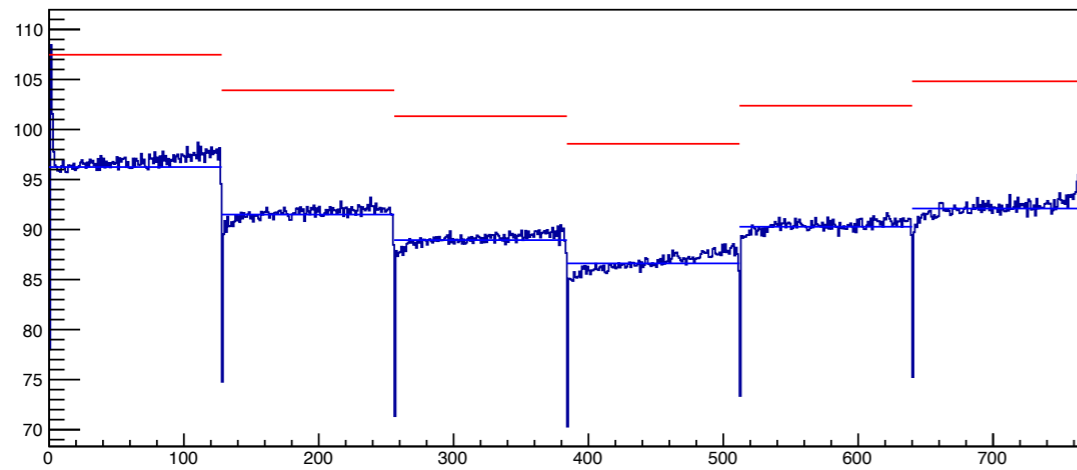
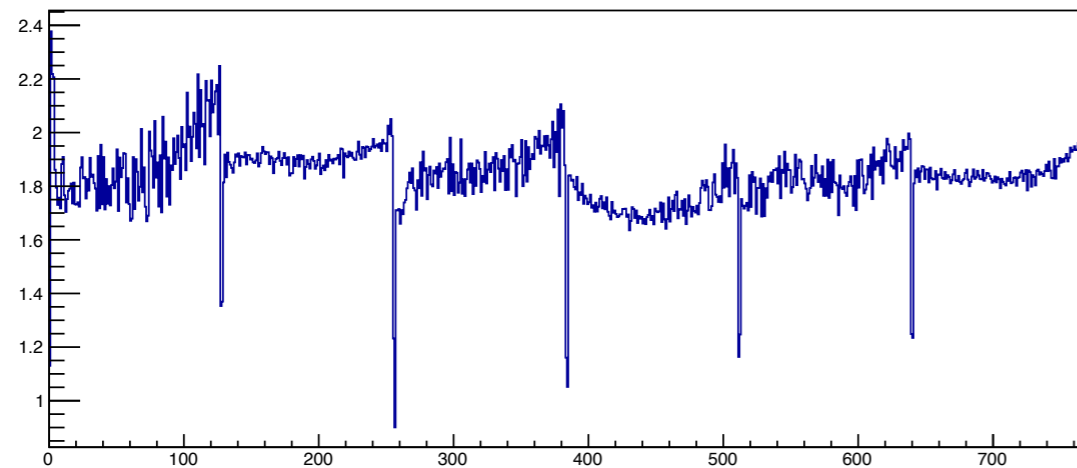


# Detector D , Si detector on top, when assembly faces down

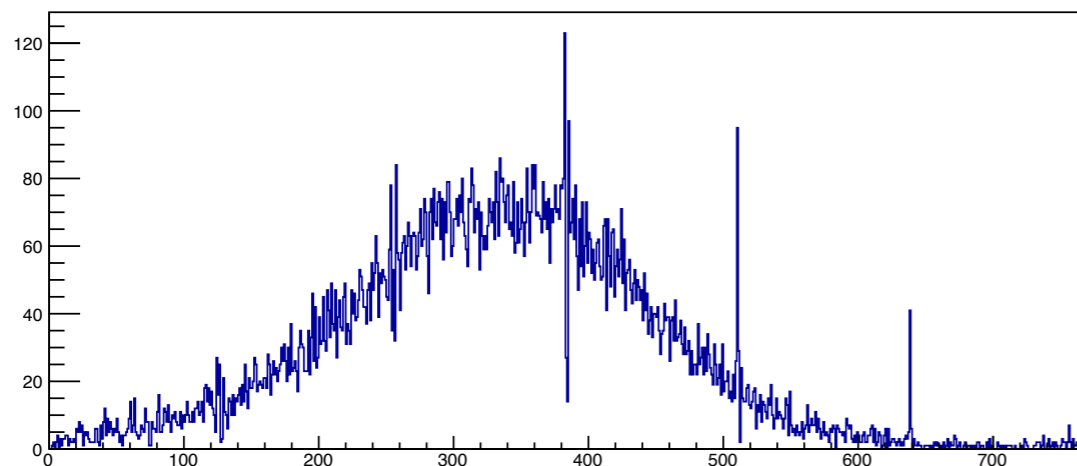
Pedestals for all strips



Pedestals Sigmas for all strips



Hit Profile distribution

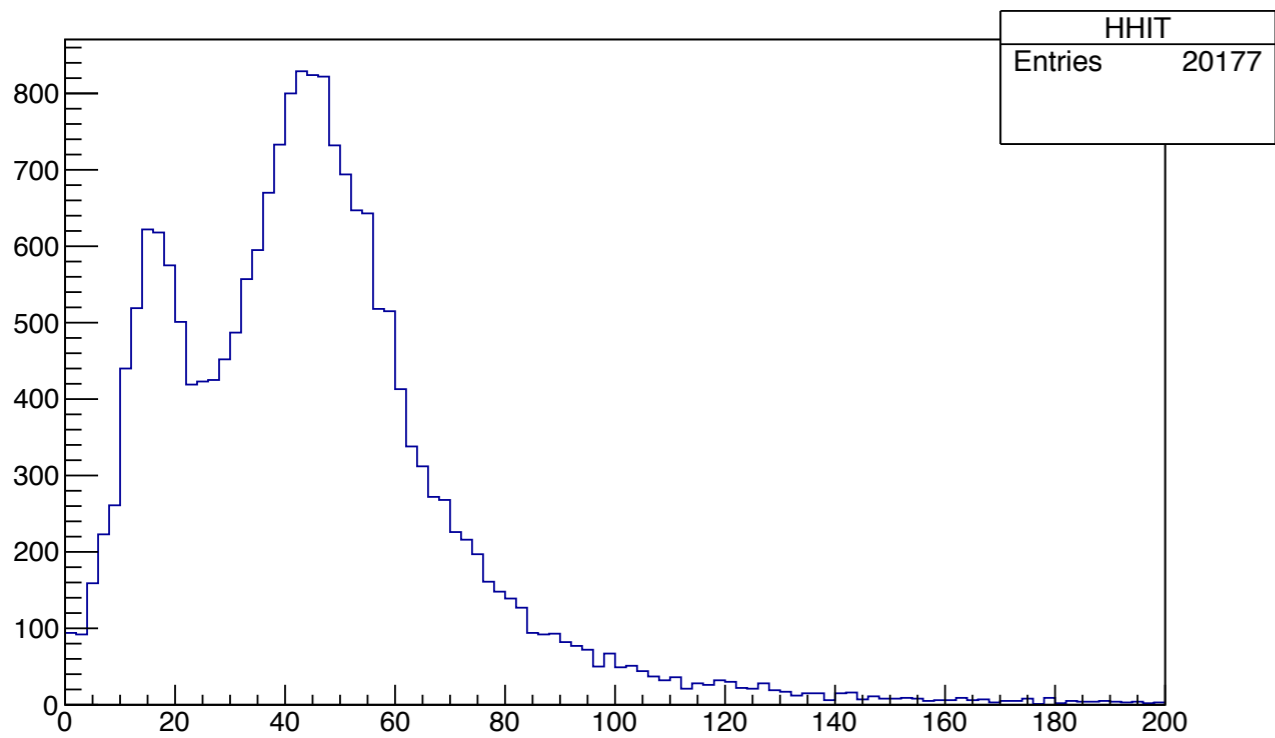


This detector has 6 SVXs

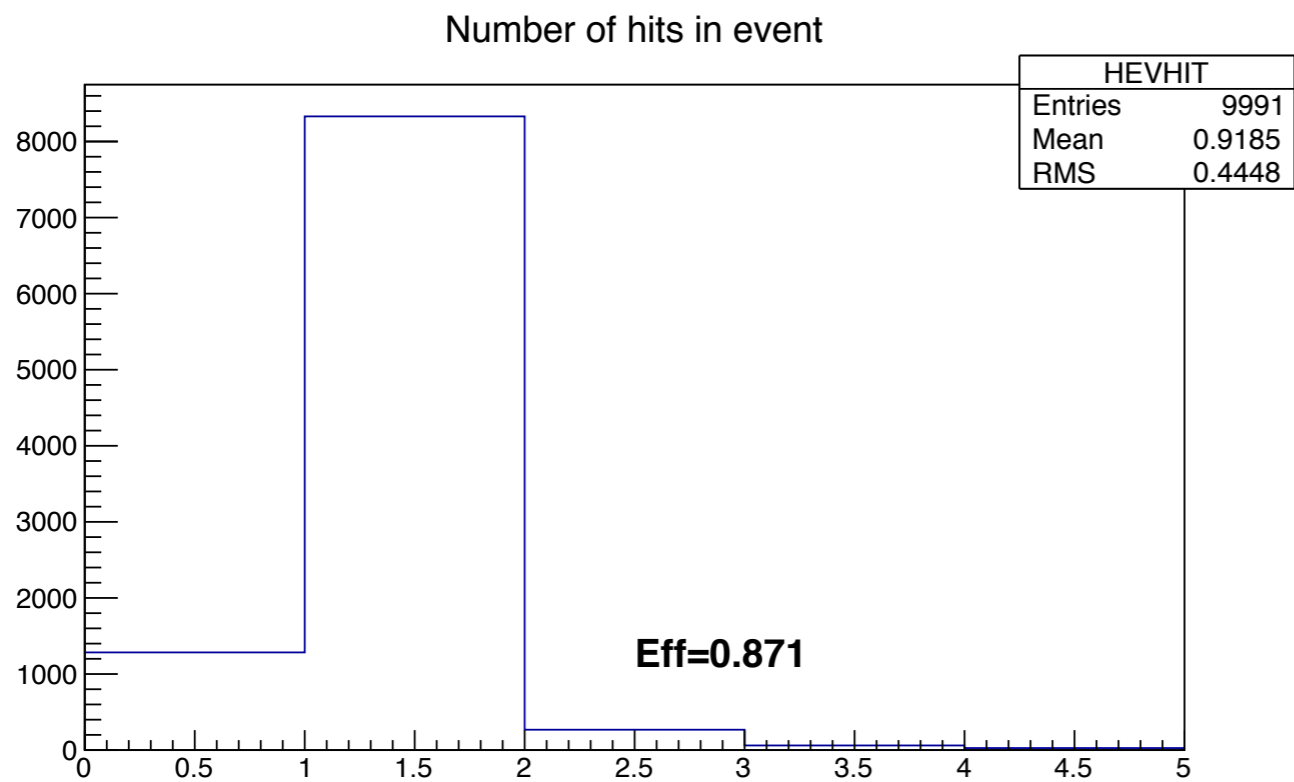
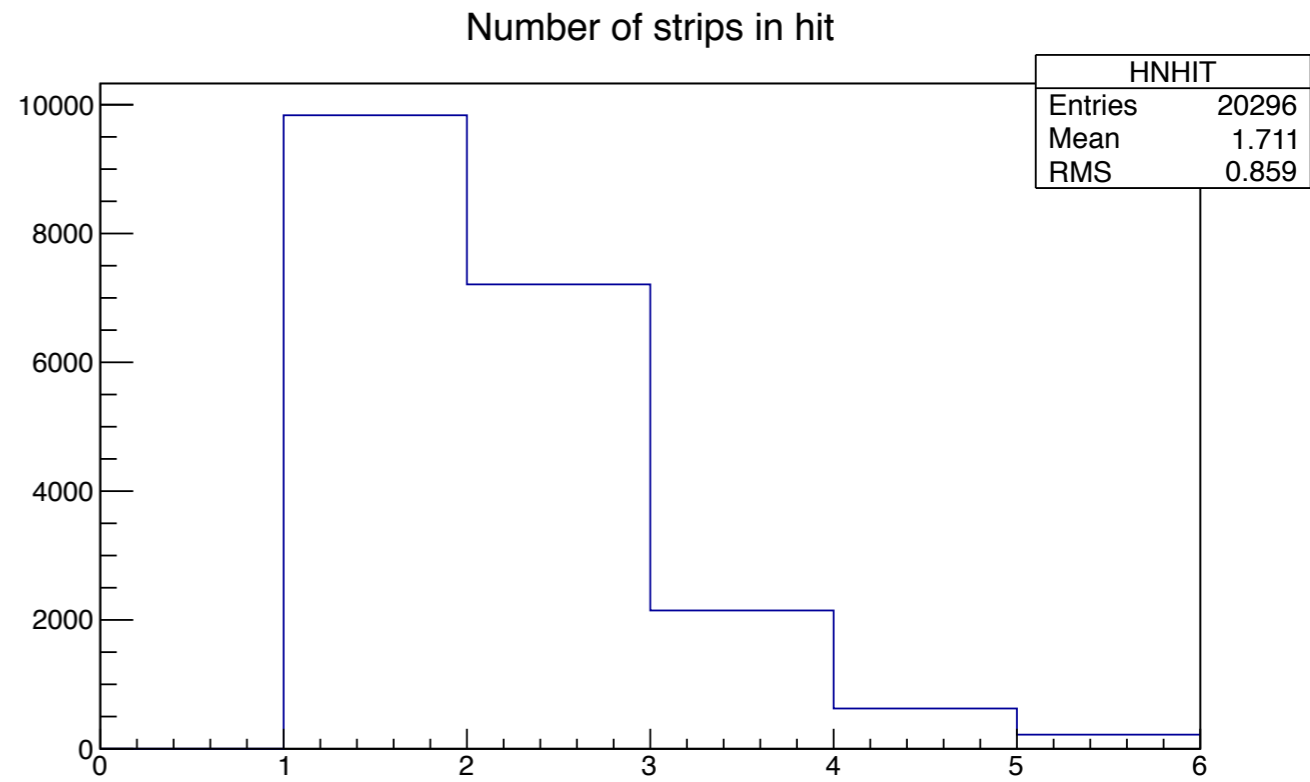
Source  $^{90}\text{Sr}$  placed close to bottom Si detector (A)

Scintillator trigger on top of detector D (same area as Si)

Thu Sep 25 11:55:42 2014



Integrated charge in cluster



Bias voltage 90 V